

# **Assignment Questions**

## **Computer Architecture and Organization**

- Q.1:-** 8086 architecture
- Q.2:-** Instruction Set: Characteristics - Operand Types - Operation Types
- Q.3:-** Addressing Modes - instruction Formats
- Q.4:-** Addressing Modes (Simple Examples)
- Q.5:-** assembly language programming
- Q.6:-** elementary assembly pipelining
- Q.7:-** interrupts
- Q.8:-** Computer Arithmetic: ALU -. Integer Representation and Arithmetic
- Q.9:-** Floating Point Representation and Arithmetic
- Q.10:-** CPU: Organization of Processors and Registers
- Q.11:-** Instruction Cycle - Instruction Pipelining
- Q.12:-** Pentium Processor
- Q.13:-** Register Optimisation - Architecture Pipelining. Instruction Set Architecture(ISA)
- Q.14:-** RISC and CISC, Compare RISC versus CISC
- Q.15:-** Characteristics of RISC, Large Register File
- Q.16:-** Characteristics of CISC, Instruction set complexity
- Q.17:-** Vector Processing Requirements, Characteristics of vector processing, Multiple vector task
- Q.18:-** Control Unit: Micro-Operations - Control of Processors
- Q.19:-** Hardwired Implementation – Micro Programmed Control Concepts
- Q.20:-** Microinstruction Sequencing - General Microinstruction Execution