

Days  
Period

Class  
Subject

Class Work

class - B.Tech 3<sup>rd</sup> yrs (E.C.E.)  
 Subject: "AD.DV"  
 Faculty: Mandeep Singh  
 Date: 24/7/2020

Topic: State Machine

Comparison of Moore and Melay Models

S.No	Moore Model	Melay Model
1.	The final O/P depends only on the present state of memory elements	The final O/P depends on the present state of memory elements and the external I/P
2.	The O/P changes only after the active clk edge	O/P changes in between the clk edges if the external I/P changes
3.	Implementations of a logic function needs more numbers of state than Melay circuit	Implementation of the same logic function require less numbers of state than Moore circuit

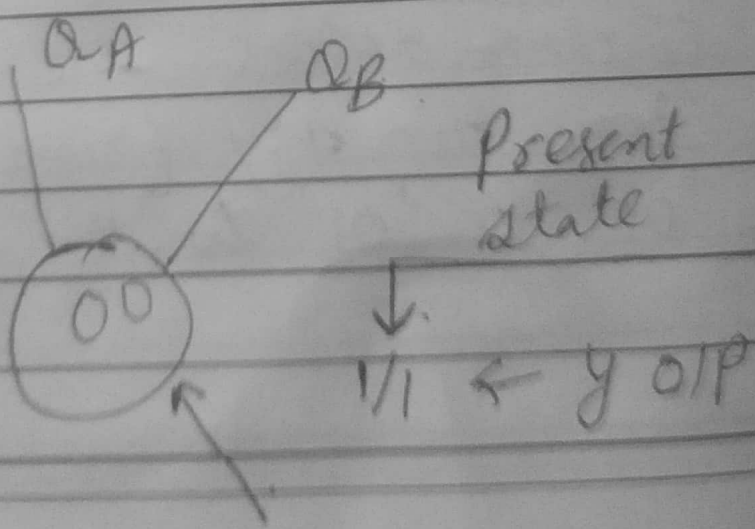
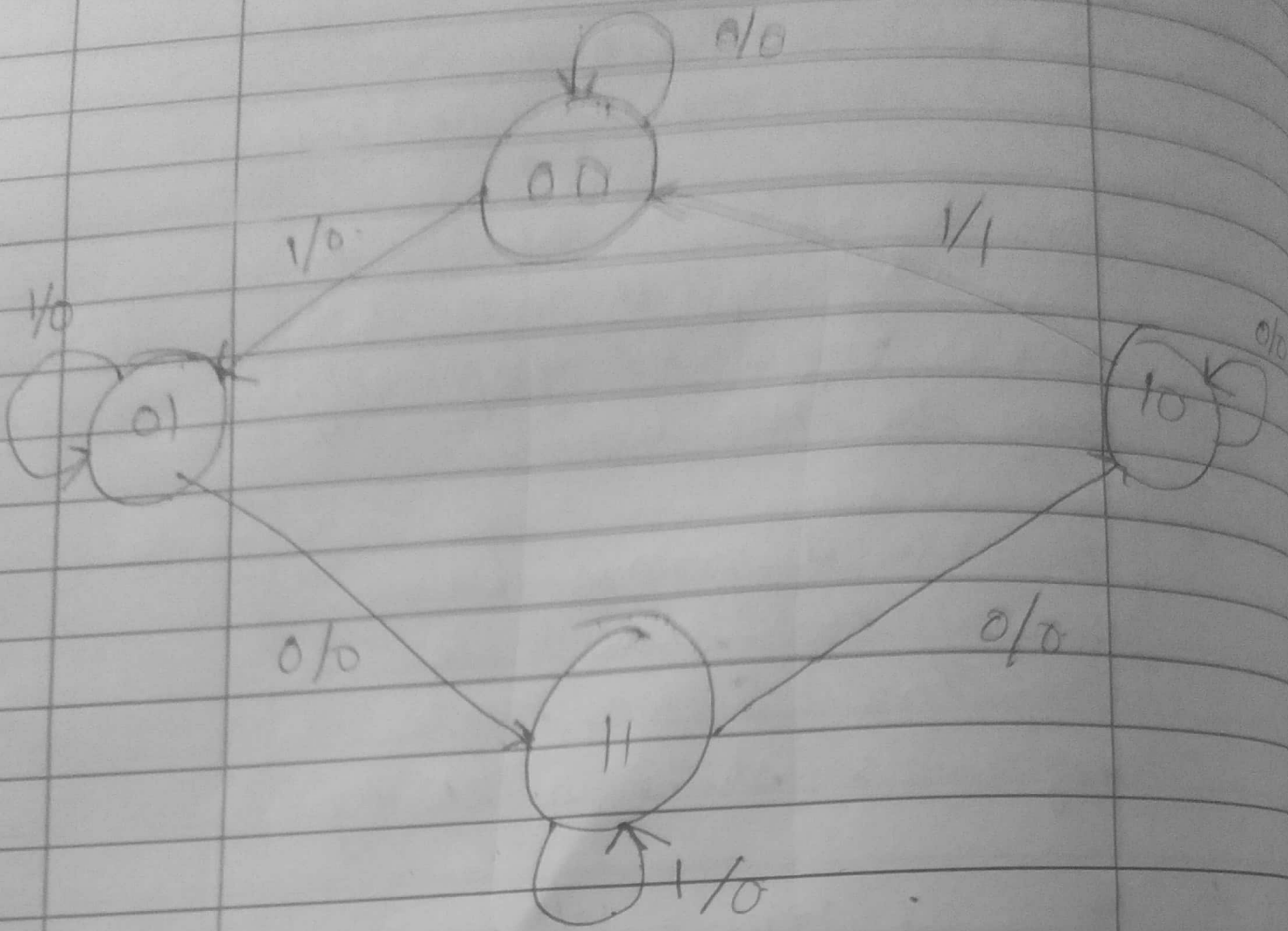
Remarks:

State table - The state table tell us the relationship between the present state, next state and o/p

State diagram - The information available in the state table is represented graphically using the state diagram.

\* The state diagram is drawn by using the state table as a reference. Such a state diagram is shown in the fig.

\* The circle represented the present state. The arrows b/w the circle define the state transition say from "00" to "01" or "01" to "11".



Remarks :

Subject: Class Work Forecast

\* A directed line same width connecting the the next state to that the present state is same as

\* The line joining the circles are labeled with a pair of binary numbers with a slash in between. For example the line joining 00 and 01 is labeled as 1/0.

\* Note that 00 to 01 transition takes place when  $x=1$  and  $y=0$  (see row -1 of the state table).

Hence 1 in 1/0 corresponds to "x" and "0" corresponds to y.

## General format of state Table

Present state		Next state				Output $Y$	
		$x=0$		$x=1$		$x=0$	$x=1$
QA	QB	QA	QB	QA	QB		
0	0	0	0	0	1	0	0
0	1	1	1	0	1	0	0
1	0	1	0	0	0	0	1
1	1	1	0	1	1	1	0

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