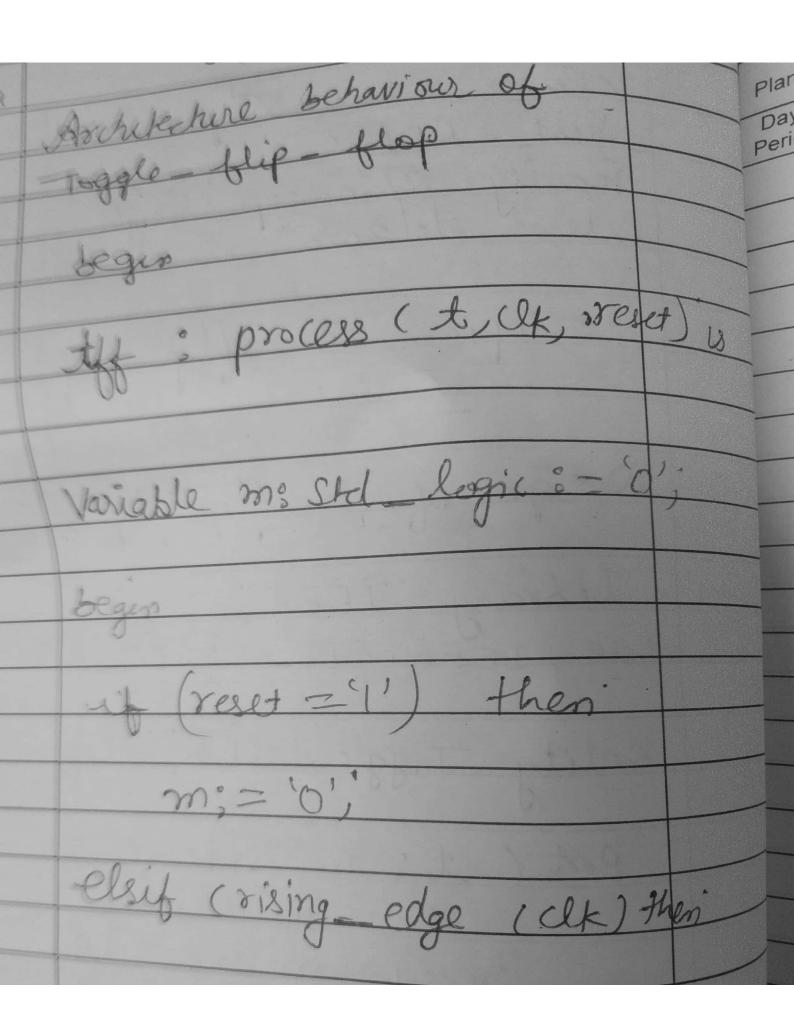
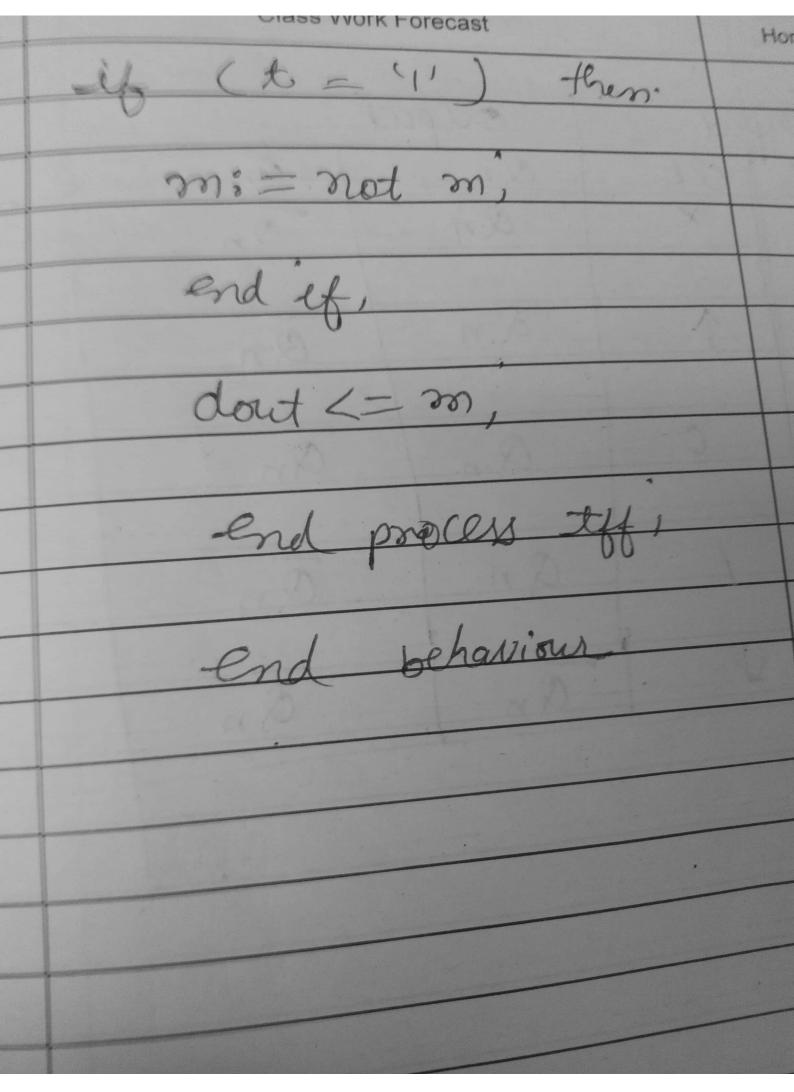
class- B. Fech 3th yn ECE Sust APPV Paculty + Mandeep Singh Topic + T- flip flop Design of T- flip flop lebrary IEEE; use JEEE. STD\_ LOGIC\_ 1164.all; entity Toggle-flip flop is port (t; in Std-logic; Ot: in Std-logic; dout: out std logic); end Toggle-flip-flopi





Scanned with CamScanner

| Input  |     | Output on |       |
|--------|-----|-----------|-------|
| To the | clk | Qn        | Tom . |
| 0      | X   | Qn        | an    |
|        |     |           |       |
| 1      | 1.  | Qn        | Qn    |
|        |     | (6)       |       |
| 1      | 0.  | Qn/       | 9n    |
|        |     |           |       |
|        |     | 1 an      | (Qn)  |
| 1      | 1.  | 1-0       | On    |
| *      |     | I Un J.   |       |
|        |     |           |       |
|        |     |           |       |

