KEC402 Analog Circuits	3L:1T:0P	4 Credits	
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Unit	Topics	Lectures
I	Diode circuits, amplifier models: Voltage amplifier, current amplifier, trans-conductance amplifier and trans-resistance amplifier biasing schemes for BJT and FET amplifiers, bias stability, various configurations (such as CE/CS, CB/CG, CC/CD) and their features, small signal analysis, low frequency transistor models, estimation of voltage gain, input resistance, output resistance etc., design procedure for particular specifications, low frequency analysis of multistage amplifiers.	8
II	High frequency transistor models, frequency response of single stage and multistage amplifiers, cascode amplifier, various classes of operation (Class A, B, AB, C etc.), their power efficiency and linearity issues, feedback topologies: Voltage series, current series, voltage shunt, current shunt, effect of feedback on gain, bandwidth etc., calculation with practical circuits, concept of stability, gain margin and phase margin.	8
III	Oscillators: Review of the basic concept, Barkhausen criterion, RC oscillators (phase shift, Wien bridge etc.), LC oscillators (Hartley, Colpitt, Clapp etc.), non-sinusoidal oscillators.	8
IV	Current mirror: Basic topology and its variants, V-I characteristics, output resistance and minimum sustainable voltage (VON), maximum usable load, differential amplifier: Basic structure and principle of operation, calculation of differential gain, common mode gain, CMRR and ICMR, Op-Amp design: Design of differential amplifier for a given specification, design of gain stages and output stages, compensation.	8
V	Op-Amp applications: Review of inverting and non-inverting amplifiers, integrator and differentiator, summing amplifier, precision rectifier, Schmitt trigger and its applications, active filters: Low pass, high pass, band pass and band stop, design guidelines.	8